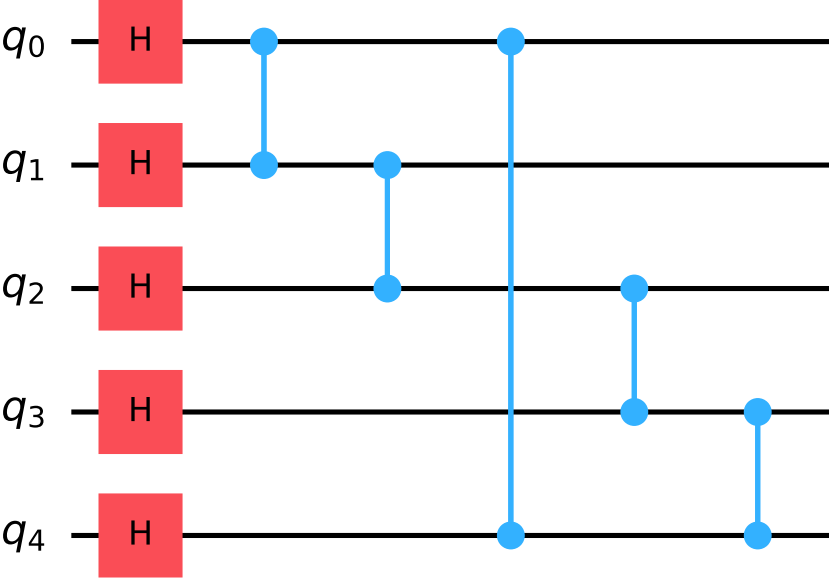




Circuit Name	Graph state
Width	5
Depth	5
Total Gates	10
Non-local Gates	5